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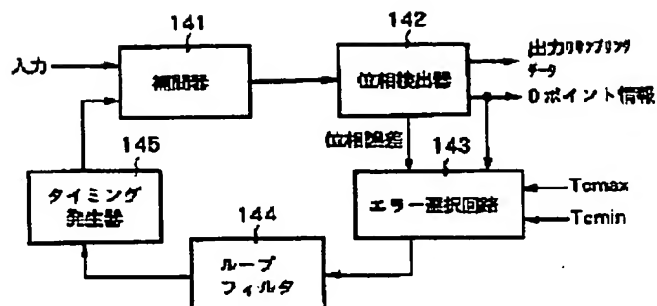
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APPLICANT : VICTOR CO OF JAPAN LTD;

INVENTOR : TONAMI JUNICHIRO;

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TITLE : DIGITAL SIGNAL REPRODUCING
DEVICE



ABSTRACT : PROBLEM TO BE SOLVED: To provide a digital signal reproducing device capable of surely reproducing recorded information on a recording medium while stably following a phase without inducing phase oscillation or bit slip in the device itself.

SOLUTION: Concerning a resampling DPLL for generating and outputting resampling data by resampling a digital regenerative signal at a desired bit rate, this resampling DPLL is composed of an interpolator 141, a phase detector 142 for generating and outputting the resampling data and generating and outputting '0' point information showing a zero cross point and a phase error signal from an input data value, an error selecting circuit 143 for receiving the phase error signal and the '0' point information as an input signal, selecting only an effective component out of the phase error signal and generating and outputting a new phase error signal by invalidating the phase error signal generated just after an inversion interval except for a set range, a loop filter 144 and a timing generator 145.

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